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** CONTINUING DATA **********************************							
** FOREIGN APPLICATIONS ************************************							
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ret Verified and Acknowledged Allowarice Examiner's Signature Initials		_T_	COUNTRY JAPAN	DRAWING 17	CLAIM 6	18	CLAIMS 4
ADDRESS 23850 ARMSTRONG, KRATZ, QUINTOS, HANSON & BROOKS, LLP 1725 K STREET, NW SUITE 1000 WASHINGTON, DC 20006							
TITLE Method of Manufacturing a Semiconductor Device and a Method for Fixing the Semiconductor Device Using Substrate Jig							